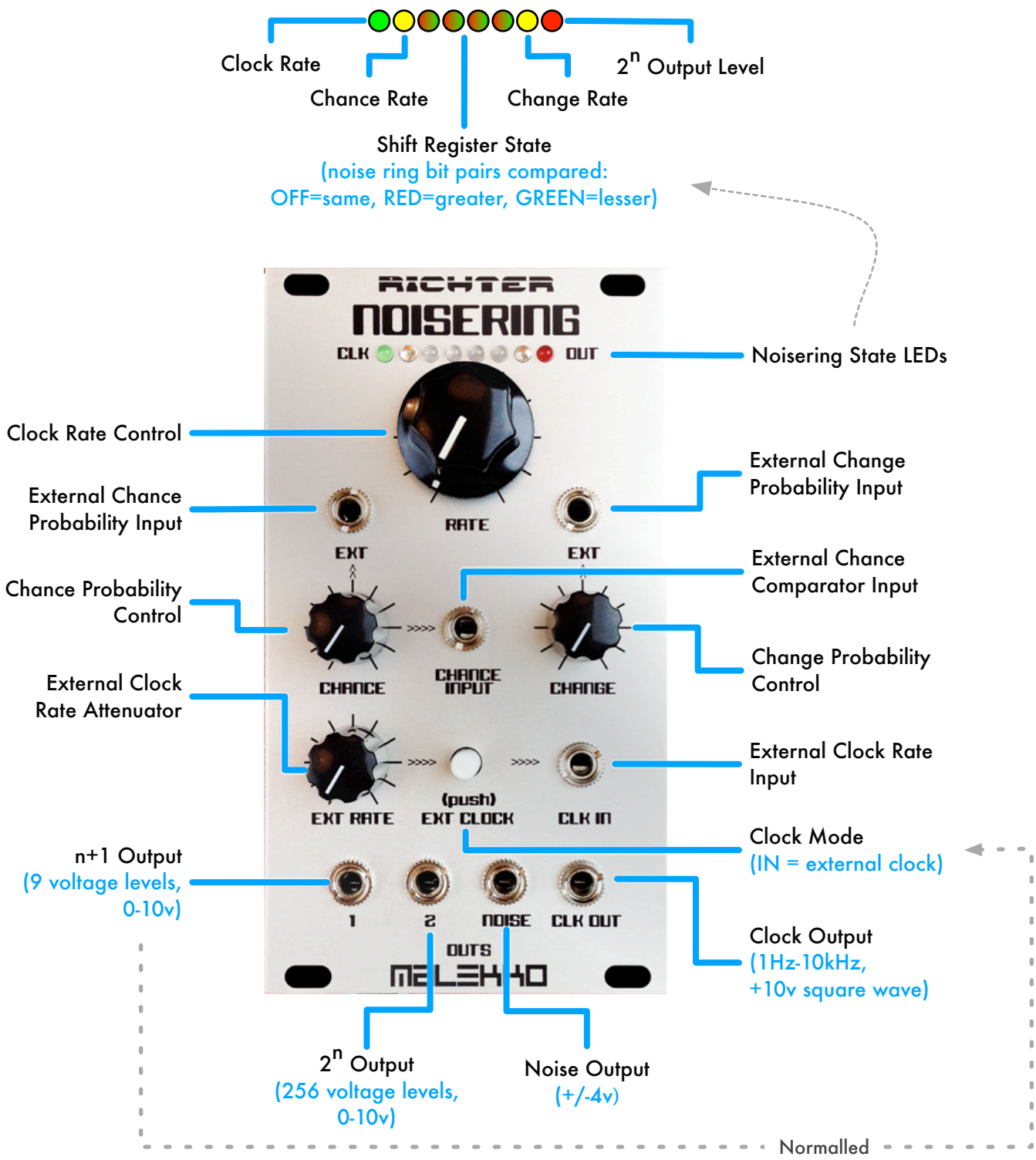
Clock Rate
Chance Rate
Change Rate
2n Output Level
Shift Register State
(noise ring bit pairs compared:
OFF=same, RED=greater, GREEN=lesser)
RICHTER
NOISERING
CLK
OUT
Noisering State LEDs
Clock Rate Control
RATE
External Change
Probability Input
External Chance
Probability Input
EXT
EXT
External Chance
Comparator Input
Chance Probability
Control
CHANCE
CHANCE
INPUT
CHANGE
Change Probability
Control
External Clock
Rate Attenuator
EXT RATE
(push)
EXT CLOCK
CLK IN
External Clock Rate
Input
Clock Mode
(IN = external clock)
n+1 Output
(9 voltage levels,
0-10v)
1
2
NOISE
CLK OUT
OUTS
MELEKTRO
Clock Output
(1Hz-10kHz,
+10v square wave)
2n Output
(256 voltage levels,
0-10v)
Noise Output
(+/-4v)
Normalled